



Feature Identification Device (FID) EEPROM Specification

For Wasatch Photonics Spectrometers

Revision 8
Mar 23, 2020

Revision Log

Revision	Date	By	Reason
1.2	2017-25-1	J. Traud	Formatting, updated FID protocol to include coefficients on device.
2	2017-18-7	J. Traud	Added Excitation to page 1. Added bad pixel allotment to page 5.
3	2018-05-29	J. Traud	Updated spec to include two sets of GAIN and OFFSET values as well as calibration information for output laser power (UNRELEASED)
4	internal	M. Zieg R. Dickerson	<ul style="list-style-type: none"> • Added floating-point excitation wavelength • changed revisioning from per-page to entire EEPROM • changed ints to uint where negatives were invalid • moved min/maxIntegrationTimeMS to fit 24-bit range
5	internal	M. Zieg	added productConfiguration
6	2018-18-09	M. Zieg	added Raman intensity calibration
7	internal	T. Stohrer	added Average FWHM (<i>nm or cm⁻¹ per Excitation</i>)
8	2020-03-23	M. Zieg	added page 6/7 subformat

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1. General Description

This document details the method for identifying the model, serial number, configuration settings and features of a Wasatch Photonics spectrometer. This document does not describe spectrometers designed for the OCT market such as the Cobra series.

This specification requires one of the following firmware part numbers and the associated revision or later:

Table 1 FID PID and Firmware

PID	USB Descriptor	Firmware P/N	Firmware Description
0x1000	WP FX2 Silicon	170003	WP USB Board FX2 Code
0x2000	WP FX2 InGaAs	170037	WP InGaAs USB Board FX2 Code
0x4000	WP ARM	170019	WP ARM USB Board

Older Wasatch Photonics spectrometers used USB PID or descriptors to define the model and serial number. With Feature Identification, the USB PID and descriptor are generic for a given board type. Model and serial number information is stored in the device's non-volatile EEPROM storage, along with many other per-device settings and calibrations. Firmware revision numbers are standardized as four hex bytes, starting at 10.0.0.0.

2. Product Feature Categories

Following are the categories for product features and the options for each feature.

2.1. Laser type

Below are the different laser options the product can have.

- No laser — The product has no laser, so no laser commands are applicable. These units do not support commands to read laser temperature or modulating the laser.
- Internal laser — The product contains its own internal laser. This means the laser temperature can be read and the laser can be modulated.
- External laser — The product does not contain an internal laser but may be able to control an external laser. The laser temperature can't be read but the external laser can be modulated.

2.2. Laser control

A product with a laser can modulate the laser output to control the laser power. The modulation can either be based on a frequency or it can be determined by transition points. In the place of laser modulation, a ramping function can be implemented to slowly ramp the laser power up when the laser is turned on.

Laser modulation

If laser modulation is present and enabled, the following parameters apply:

- Laser modulation duration — If the laser modulation is linked to the integration time, the modulation will last for the number of microseconds set for the laser modulation duration.
- Laser modulation delay time — If the laser modulation is linked to the integration time, the laser modulation delay time will delay the start of laser modulation a set time after integration has started.
- Laser modulation period — period of the laser modulation (μs)
- Laser modulation pulse width — the duty cycle of the modulation and therefore laser power (μs)

Laser transition points

Instead of modulation, the laser is controlled with transition points. When a transition point occurs, the laser will state will toggle (off \rightarrow on, on \rightarrow off). Six transition points are available.

Laser ramping

In place of laser modulation or laser transition points, laser ramping is used to slowly ramp up laser power when the laser is turned on.

2.3. Sensor cooling

Cooled sensor

The product has a cooled sensor, which means the product has TEC circuitry that can maintain the sensor temperature at a programmable setpoint. The SET_DETECTOR_TEC_SETPOINT command is used to program the setpoint, and the detector temperature can be read back with GET_DETECTOR_TEMPERATURE.

Non-cooled sensor

The sensor in the product is not being cooled. There is no temperature setpoint (no TEC), and the CCD temperature can't be read back (no thermistor).

2.4. Integration resolution

This parameter specifies how much time one bit of the integration time represents. Depending on the product, the integration resolution can be either 1 ms, 10 ms or switchable between 1 and 10ms.

2.5. Horizontal binning

This parameter specifies whether horizontal binning is available.

2.6. Line length

This parameter specifies the number of pixels are read back from the detector in a linear row. The reported line length is based on the physical detector characteristics, and does not include onboard post-processing like horizontal binning.

2.7. CF Select (Has High-Gain Mode)

This parameter indicates whether or not there is a low gain/high gain setting for the sensor. The InGaAs sensor used for NIR systems has a dedicated pin (CF_SELECT) that switches between low gain and high gain. This is not related

2.8. Area Scan

Some CCD and CMOS-based products can be placed in "area scan mode," and instead of reading out a single vertically-binned one-dimensional array representing the complete set of photons collected across the entire detector, the spectrometer will send each individual line or row of detector data. These lines can be aggregated in software over time to form an image that can be used for alignment or visualization purposes.

2.9. Battery

The SiG and certain OEM products have battery power.

3. FPGA Compilation Options

The FPGA can be compiled to enable or disable different features. The FPGA Compilation Options register can be read to indicate which features are enabled. Below is the format for the register.

Table 2 FPGA Compilation Options Register

Bit	Description	Possible Values
[2-0]	Integration time resolution	0: 1 ms 1: 10 ms 2: Switchable between 1 us and 1 ms. 3-7: <i>UNDEFINED</i>
[5-3]	Data header or tag	0: No header or tag 1: Ocean Optics header and tag 2: Wasatch tag 3-7: <i>UNDEFINED</i>
[6]	Cf Select (Only available on InGaAs)	0: High-Gain mode not available 1: High-Gain mode available
[8-7]	Laser	0: No laser 1: Internal laser 2: External laser 3: <i>UNDEFINED</i>
[11-9]	Laser control	0: Laser modulation 1: Laser transition points 2: Laser ramping 3-7: <i>UNDEFINED</i>
[12]	Area scan feature	0: Not available 1: Available
[13]	Actual integration time feature	0: Not available 1: Available
[14]	Horizontal binning feature	0: Not available 1: Available
[15]	Undefined	Available for new features

4. Model Information

The EEPROM contains 8 pages of 64 bytes each (512 bytes total). EEPROM pages are read and written as raw buffers by the firmware. The firmware does not attempt to read or parse individual fields within the pages, therefore the internal format and structure of EEPROM pages can change and evolve over time without recompiling the firmware.

The last byte of the first page (page 0, byte 63) is a format revision number for the first 6 pages EEPROM (pages 0-5). The last byte of the 6th page (page 5, byte 63) is a format revision number for the last 2 EEPROM pages (pages 6-7)

Table 3 EEPROM Page Overview

Page	General Function
0	Device identification and features
1	Device calibration
2	Detector configuration
3	Lifetime usage statistics
4	Customer data
5	Bad pixel configuration
6	<i>Custom</i>
7	<i>Custom</i>

The following datatypes are referenced in the EEPROM field definitions:

- `char[]` — an ASCII string of the given maximum length. If a value less than the maximum is written to the field, at least one trailing null (`'\0'`) should be used as a C-style string terminator. If the full field length is used, no terminating null is required.
- `bool` — although physically stored as a `uint8` (unsigned char), field is logically a Boolean and only values of 0 and 1 are guaranteed supported.
- `float32` — these are 4-byte IEEE 754 Float
- `byte` — these values may be internally treated as enums; see relevant command documentation

Major changes since the last public release are marked in **red**.

Table 4 EEPROM Page Format

Page	Size	Byte	Description	Format
0	64	0-15	Model name	char[16]
		16-31	Serial number	char[16]
		32-35	Baud rate	uint32
		36	Cooling available	bool
		37	Battery available	bool
		38	Laser available	bool
		39-40	<i>Unused</i> ¹	
		41-42	Slit size in um	uint16
		43-44	Startup Integration Time in ms	uint16
		45-46	Startup Temperature in °C	int16
		47	Startup Triggering Mode	byte
		48-51	Gain (for InGaAs: Even Pixel Gain) ²	float32
		52-53	Offset (for InGaAs: Even Pixel Offset) ²	int16
		54-57	Odd Pixel Gain (InGaAs systems only) ²	float32
		58-59	Odd Pixel Offset (InGaAs systems only) ²	int16
		60-62	<i>Unused</i>	
		63	EEPROM format revision = 8	byte

Page	Size	Byte	Description	Format
1	64	0-3	Wavelength calibration Coeff ₀	float32
		4-7	Wavelength calibration Coeff ₁	float32
		8-11	Wavelength calibration Coeff ₂	float32
		12-15	Wavelength calibration Coeff ₃	float32
		16-19	°C → DAC TEC Coeff ₀	float32
		20-23	°C → DAC TEC Coeff ₁	float32
		24-27	°C → DAC TEC Coeff ₂	float32
		28-29	T _{max} (max TEC setpoint in °C)	int16
		30-31	T _{min} (min TEC setpoint in °C)	int16
		32-35	ADC → °C Detector Temperature Coeff ₀	float32
		36-39	ADC → °C Detector Temperature Coeff ₁	float32
		40-43	ADC → °C Detector Temperature Coeff ₂	float32
		44-45	Thermistor Resistance at 298K	int16
		46-47	Thermistor Beta Value	int16
		48-59	Calibration Date	char[12]
		60-62	Calibrated By	char[3]
		63	<i>Unused</i>	

¹ Page formats <= 3 treat this as integral excitation wavelength; formats >= 4 move that to page 3

² InGaAs products use two registers for gain and two for offset. This allows for the even and odd pixels to be adjusted independently. All other products use the singular gain and offset register found on bytes 48 through 53

Page	Size	Byte	Description	Format
2	64	0-15	Detector Name	char[16]
		16-17	Active Pixels Horizontal	uint16
		18	<i>Unused</i>	
		19-20	Active Pixels Vertical	uint16
		21-24	<i>Wavelength Calibration Coeff₄</i>	<i>float32</i>
		25-26	Actual Horizontal Pixels	uint16
		27-28	ROI Horizontal Start	uint16
		29-30	ROI Horizontal End	uint16
		31-32	ROI Vertical Region 1 Start	uint16
		33-34	ROI Vertical Region 1 End	uint16
		35-36	ROI Vertical Region 2 Start	uint16
		37-38	ROI Vertical Region 2 End	uint16
		39-40	ROI Vertical Region 3 Start	uint16
		41-42	ROI Vertical Region 3 End	uint16
		43-46	<i>Reserved: Linearity Coeff₀³</i>	float32
		47-50	<i>Reserved: Linearity Coeff₁</i>	float32
		51-54	<i>Reserved: Linearity Coeff₂</i>	float32
		55-58	<i>Reserved: Linearity Coeff₃</i>	float32
		59-62	<i>Reserved: Linearity Coeff₄</i>	float32
		63	<i>Unused</i>	

Page	Size	Byte	Description	Format
3	64	0-3	<i>Reserved: Device lifetime operation (minutes)</i>	uint32
		4-7	<i>Reserved: Laser lifetime operation (minutes)</i>	uint32
		8-9	<i>Reserved: Max laser temperature (°C)</i>	int16
		10-11	<i>Reserved: Min laser temperature (°C)</i>	int16
		12-15	Laser Power perc → mW Coefficient 0 ⁴	float32
		16-19	Laser Power perc → mW Coefficient 1	float32
		20-23	Laser Power perc → mW Coefficient 2	float32
		24-27	Laser Power perc → mW Coefficient 3	float32
		28-31	Maximum Laser Power (mW)	float32
		32-35	Minimum Laser Power (mW)	float32
		36-39	Excitation Wavelength (nm) ⁵	float32
		40-43	Min Integration Time (ms, 24-bit)	uint32
		44-47	Max Integration Time (ms, 24-bit)	uint32
		48-51	<i>Average FWHM (nm or cm⁻¹ per Excitation)</i>	<i>float32</i>
		52-63	<i>Unused</i>	

Page	Size	Byte	Description	Format
4	64	0-63	User Text String	char[64]

³ Linearity Coeffs 0-3 have been used to provide laser power calibration in photodiode-equipped systems

⁴ Not all spectrometers receive laser power calibration or min/max thresholds

⁵ Floating-point version of excitation wavelength (nm) in version 4+

Page	Size	Byte	Description	Format
5	64	0-1	Bad Pixel 1 ⁶	int16
		2-3	Bad Pixel 2	int16
		4-5	Bad Pixel 3	int16
		6-7	Bad Pixel 4	int16
		8-9	Bad Pixel 5	int16
		10-11	Bad Pixel 6	int16
		12-13	Bad Pixel 7	int16
		14-15	Bad Pixel 8	int16
		16-17	Bad Pixel 9	int16
		18-19	Bad Pixel 10	int16
		20-21	Bad Pixel 11	int16
		22-23	Bad Pixel 12	int16
		24-25	Bad Pixel 13	int16
		26-27	Bad Pixel 14	int16
		28-29	Bad Pixel 15	int16
		30-45	productConfiguration	char[16]
		46-62	<i>Unused</i>	
		63	Page 6 and 7 subformat	byte

5. Custom EEPROM Structure

The last two EEPROM pages can be configured to hold a variety of different fields and structures, depending on the sub-format code in the last byte of page 5:

Subformat	Page 6 and 7 Contents
0	User Data
1	NIST SRM Raman Intensity Calibration
2	Advanced Wavelength Calibration Spline
3	<i>Reserved</i>
4-255	<i>Undefined</i>

5.1. Custom Format 1: NIST SRM Raman Intensity Calibration

This format is used for Raman spectrometers for which a NIST-standard SRM Raman Intensity Calibration has been provided.

The first byte of the Raman Intensity Calibration section indicates the meaning of the following 7 floating-point fields.

- A value of 0 indicates no calibration is available.

⁶ A value of -1 in any “Bad Pixel” field represents “N/A”; otherwise, the value indicates the (zero-based) pixel index that should be rejected (typically by averaging adjacent pixels in software)

- A value of 1-7 indicates a polynomial calibration of order n . That is, a value of 7 would indicate that the calibration is provided as a 7th-order polynomial, and that the next 8 floats represent coefficients 0 (x^0) through 7 (x^7).

Page	Size	Byte	Description	Format
6	64	0	Raman Intensity Calibration format	byte
		1-4	Coeff 0	float32
		5-8	Coeff 1	float32
		9-12	Coeff 2	float32
		13-16	Coeff 3	float32
		17-20	Coeff 4	float32
		21-24	Coeff 5	float32
		25-28	Coeff 6	float32
		29-32	Coeff 7	float32
		33-63	Unused	

Page	Size	Byte	Description	Format
7	64	0-63	Unused	

In order to normalize precision, the polynomial has been curve-fit against \log_{10} of the actual Raman intensity scaling factors. Therefore, to generate and apply the scaling factors for each pixel, you would do something like the following in application code (example taken from Wasatch.PY's [wasatch.SpectrometerSettings.update_raman_intensity_factors](#)):

```
##
# @param eeprom (Input) populated wasatch.EEPROM object
# @returns Raman intensity correction factors (one per pixel)
#         as 1D numpy array
def expand_raman_intensity_factors(eeprom):
    if 1 <= eeprom.raman_intensity_calibration_order <= 7:
        coeffs = eeprom.raman_intensity_coeffs
        if coeffs is not None:
            try:
                factors = []
                for pixel in range(self.pixels()):
                    log10_factor = 0.0
                    for i in range(len(coeffs)):
                        x_to_i = math.pow(pixel, i)
                        scaled = coeffs[i] * x_to_i
                        log10_factor += scaled

                    expanded = math.pow(10, log10_factor)
                    factors.append(expanded)
                return numpy.array(factors, dtype=numpy.float64)
            except:
                print("error generating intensity factors")
    return None
```

```
##
# @param spectrum (Input) uncorrected Raman spectrum as 1D
#                               numpy array
# @param factors (Input) Raman intensity correction factors
#                               one per pixel) as 1D numpy array
# @returns corrected Raman spectrum as 1D numpy array
def apply_raman_intensity_factors(spectrum, factors):
    return spectrum * factors
```

5.2. Custom Format 2: Advanced Wavelength Calibration Spline

This format is being proposed for customers who wish to calibrate their spectrometer's wavelength x-axis using a cubic spline fit.

A typical n-point spline would include:

- n (the number of points in the spline)
- n floating-point wavelengths
- n floating-point y values
- n floating-point y2 values
- first and last valid wavelength (optional but recommended)

So a 12-point spline would need to store n itself (value 12, 1 byte), $3n$ floats for the spline points (144 bytes), plus 2 min/max floats (8 bytes), for 153 bytes. That doesn't fit into two 64-byte pages, so for this feature we're rolling-in page 4 as well (normally reserved for User Data).

The spline wavecal would be expanded using the *splint()* function found in section 3.3 of Numerical Recipes in C (1986) (reproduced here for posterity), where:

- xa = the array of wavelengths used to generate the spline (e.g., array of the 12 Wavelength values)
- ya = array of y-values
- $y2a$ = array of $y2$ (y'') values
- n = size of the arrays (e.g. 12)
- x = the wavelength for which you want the corresponding fractional pixel generated

(Note that spline wavecal is designed to generate pixel from wavelength, the opposite of our standard wavelength calibration generation.)

```
float splint(float *xa, float *ya, float *y2a, int n, float x){
    int klo = 0;
    int khi = n - 1;

    while (khi - klo > 1) {
        int k = (khi + klo) >> 1;
        if (xa[k] > x)
            khi = k;
        else
            klo = k;
    }
}
```

```

float h = xa[khi] - xa[klo];
if (h == 0.0)
    throw("Bad XA input");

float a = (xa[khi] - x) / h;
float b = (x - xa[klo]) / h;
return a * ya[klo]
    + b * ya[khi]
    + ((a*a*a-a) * y2a[klo] + (b*b*b-b)*y2a[khi]) * (h*h)/6.0;
}
    
```

Page	Size	Byte	Description	Format
6	64	0	Spline Point Count (0 - 14)	byte
		1-3	<i>Unused</i>	
		4-7	Wavelength ₀	float32
		8-11	Y ₀	float32
		12-15	Y ₂₀	float32
		16-19	Wavelength ₁	float32
		20-23	Y ₁	float32
		24-27	Y ₂₁	float32
		28-31	Wavelength ₂	float32
		32-35	Y ₂	float32
		36-39	Y ₂₂	float32
		40-43	Wavelength ₃	float32
		44-47	Y ₃	float32
		48-51	Y ₂₃	float32
		52-55	Wavelength ₄	float32
		56-59	Y ₄	float32
		60-63	Y ₂₄	float32

Page	Size	Byte	Description	Format
7	64	0-3	Wavelength ₅	float32
		4-7	Y ₅	float32
		8-11	Y2 ₅	float32
		12-15	Wavelength ₆	float32
		16-19	Y ₆	float32
		20-23	Y2 ₆	float32
		24-27	Wavelength ₇	float32
		28-31	Y ₇	float32
		32-35	Y2 ₇	float32
		36-39	Wavelength ₈	float32
		40-43	Y ₈	float32
		44-47	Y2 ₈	float32
		48-51	Wavelength ₉	float32
		52-55	Y ₉	float32
		56-59	Y2 ₉	float32
		60-63	<i>Unused</i>	

Page	Size	Byte	Description	Format
4	64	0-3	Wavelength ₁₀	float32
		4-7	Y ₁₀	float32
		8-11	Y2 ₁₀	float32
		12-15	Wavelength ₁₁	float32
		16-19	Y ₁₁	float32
		20-23	Y2 ₁₁	float32
		24-27	Wavelength ₁₂	float32
		28-31	Y ₁₂	float32
		32-35	Y2 ₁₂	float32
		36-39	Wavelength ₁₃	float32
		40-43	Y ₁₃	float32
		44-47	Y2 ₁₃	float32
		48-55	<i>Unused</i>	
		56-59	Wavelength Minimum	float32
		60-63	Wavelength Maximum	float32